



# Aptio<sup>®</sup> V

## AMIBCP for Aptio<sup>®</sup> V

AMI BIOS Configuration Program (AMIBCP) is a powerful customization utility that enables OEMs/ODMs to customize the Aptio<sup>®</sup> ROM image without intervening on the source code and rebuilding the BIOS. With AMIBCP, it is possible to obtain multiple ROM image flavors ready for production. AMIBCP supports x86, x64 and Arm<sup>®</sup> architectures. AMI may generate a customized version of AMIBCP utility for customers' redistribution to their end users.

AMIBCP for Aptio<sup>®</sup> is an easy to use graphical tool supporting Microsoft<sup>®</sup> Windows<sup>®</sup> and Linux<sup>®</sup> operating systems.

**AMIBCP is utilized to modify several parameters:**

- Configure Aptio<sup>®</sup> system setup
- Edit the register tables
- Edit the legacy PCI routing table
- View and edit Aptio<sup>®</sup> strings
- View and modify DMI/SMBIOS tables
- View and modify Boot Order

Some of these features may not be supported by the firmware to be edited, in which case the corresponding tab will not appear.

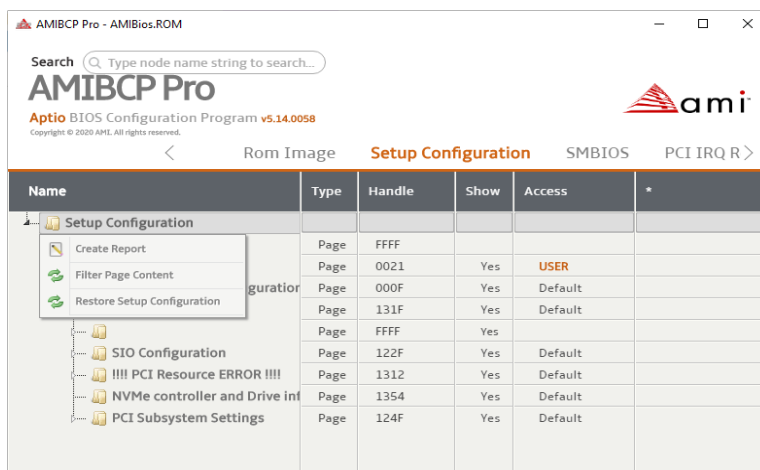
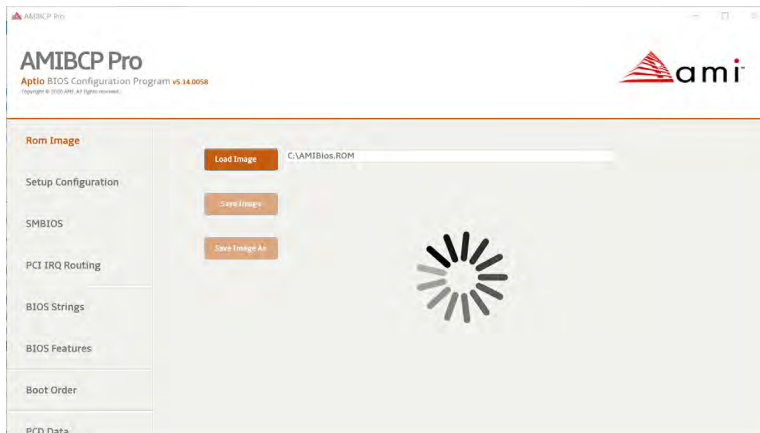


## The AMIBCP setup configuration function allows to:

- Edit setup questions
- Show or hide setup screens and setup questions
- Modify access levels and usage
- Edit failsafe and optimal values.

The PCI IRQ routing tables tab allows you to view and modify the PCI IRQ routing table that is used by the BIOS.

The BIOS Features tab allows you to view and configure some of the Aptio® features:

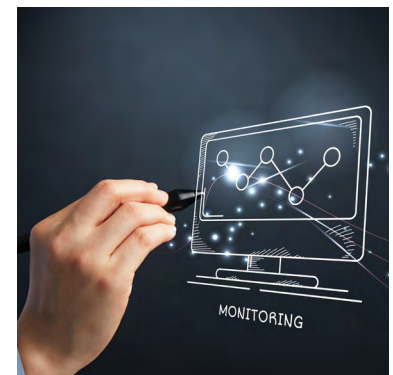


- BIOS date
- BIOS name
- Processor
- Major version
- ID string 1
- BIOS size
- BIOS tag
- Reference number
- Minor version
- CPU microcode update patches
- Sign on message
- OEM data (if available)

AMIBCP Pro - AMIBios.BIOS

**AMIBCP Pro**  
Aptio BIOS Configuration Program vS14.0058  
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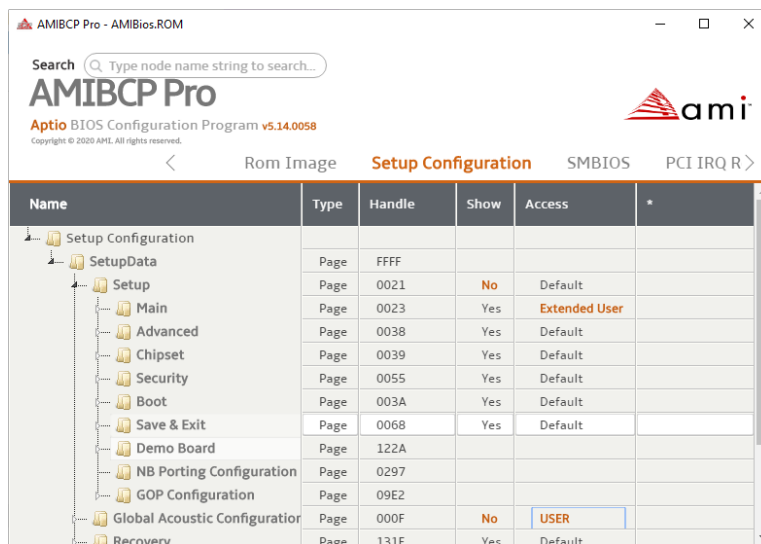
	Name	PciBusNumber	DeviceNumber	IntAReg	IntABitmap	IntBReg	IntBBitmap	IntCReg	IntCBitmap	IntDReg	IntDBitmap
Rom Image	PCI IRQ DATA										
Setup Configuration	PCI Index 0	00	00	00	0000	00	0000	00	0000	00	
	PCI Index 1	00	00	00	0000	00	0000	00	0000	00	
SMBIOS	PCI Index 2	01	01	01	DC78	02	DC78	03	DC78	04	
	PCI Index 3	02	01	00	0000	00	0000	00	0000	00	
PCI IRQ Routing	PCI Index 4	03	01	00	0000	00	0000	00	0000	00	
	PCI Index 5	00	00	01	DC78	02	DC78	03	DC78	04	
BIOS Strings	PCI Index 6	00	00	02	DC78	03	DC78	04	DC78	01	
	PCI Index 7	00	00	03	DC78	04	DC78	01	DC78	02	
BIOS Features	PCI Index 8	00	02	01	DC78	00	0000	00	0000	00	
	PCI Index 9	00	02	00	0000	00	0000	00	0000	00	
Boot Order	PCI Index 10	00	04	01	DC78	00	0000	00	0000	00	
	PCI Index 11	00	05	01	DC78	00	0000	00	0000	00	
	PCI Index 12	00	08	01	DC78	00	0000	00	0000	00	
	PCI Index 13	00	1F	01	DC78	02	DC78	03	DC78	04	
	PCI Index 14	00	1F	01	DC78	00	0000	00	0000	00	
	PCI Index 15	00	1F	01	DC78	00	0000	00	0000	00	
	PCI Index 16	00	1F	01	DC78	00	0000	00	0000	00	



The BIOS Boot Order tab allows you to configure the boot order for the initial system boot.

The DMI tables tab allows you to view and modify the SMBIOS tables which include system information, firmware information, baseboard, and more.

The firmware image generated will have to be re-signed in order to be used with AFU, but may be flashed as is making use of a flash programmer.



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